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APPLICANTS

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** CONTINUING DATA *****
 This application is a 371 of PCT/JP01/03922 05/11/2001 *verified rs*

** FOREIGN APPLICATIONS *****
 JAPAN 2000-140072 05/12/2000 *wi pd rs*

Foreign Priority claimed 35 USC 119 (a-d) conditions met Verified and Acknowledged	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance <i>2/11/02</i> Examiner's Signature Initials	STATE OR COUNTRY JAPAN	SHEETS DRAWING 16	TOTAL CLAIMS 14	INDEPENDENT CLAIMS 7
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TITLE
 Semiconductor device, mounting circuit board, method of producing the same, and method of producing mounting structure using the same

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